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FDA69N25

June 2014

N-Channel UniFETTM MOSFET 250 V, 69 A, 41 m Ω

Features

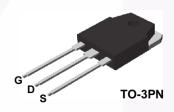
- $R_{DS(on)} = 34 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V, } I_D = 34.5 \text{ A}$
- Low Gate Charge (Typ. 77 nC)
- Low C_{rss} (Typ. 84 pF)

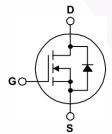
Applications

- PDP TV
- · Uninterruptible Power Supply
- AC-DC Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter			FDA69N25	Unit
V _{DSS}	Drain-Source Voltage			250	V
V _{DS(Avalanche)}	Repetitive Avalanche Voltage	(Note 1	1, 2)	300	V
I _D	Drain Current	- Continuous (T _C = 25°C)		69	Α
		- Continuous (T _C = 100°C)		44.2	Α
I _{DM}	Drain Current	- Pulsed (Note 1)		276	А
V _{GSS}	Gate-Source Voltage			± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		te 2)	1894	mJ
I _{AR}	Avalanche Current		te 1)	69	А
E _{AR}	Repetitive Avalanche Energy (Note		te 1)	48	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		te 3)	4.5	V/ns
P _D	Power Dissipation	(T _C = 25°C)		480	W
		- Derate above 25°C		3.84	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
T _L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds			300	°C

Thermal Characteristics

Symbol	Parameter	FDA69N25	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.26	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max. 40		C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDA69N25	FDA69N25	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics			-	1	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = 250 \mu\text{A}$	250			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.25		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 250 V, V _{GS} = 0 V			1	μΑ
		V _{DS} = 200 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Charac	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 34.5 A		0.034	0.041	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 34.5 A		25		S
Dynamic C	Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		3570	4640	pF
C _{oss}	Output Capacitance	f = 1 MHz	-	750	980	pF
C _{rss}	Reverse Transfer Capacitance			84	130	pF
Switching	Characteristics				-	
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 125 \text{ V}, I_D = 69 \text{ A},$		95	200	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{G} = 25 \Omega$		855	1720	ns
t _{d(off)}	Turn-Off Delay Time			130	270	ns
t _f	Turn-Off Fall Time	(Note 4)		220	450	ns
Q _g	Total Gate Charge	V _{DS} = 200 V, I _D = 69 A,	/	77	100	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V	-	24		nC
Q _{gd}	Gate-Drain Charge	(Note 4)		37		nC
Drain-Sou	rce Diode Characteristics and Maximum Rati	ings				
I _S Maximum Continuous Drain-Source Diode Forward Current					34	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				136	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 69 A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 69 A,		210		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs		5.7		μС

Notes

^{1.} Repetitive rating : pulse-width limited by maximum junction temperature.

^{2.} L = 0.64 mH, I_{AS} = 69 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C.

^{3.} $I_{SD} \le 69$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, starting $T_J = 25$ °C

^{4.} Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

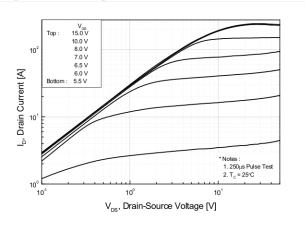


Figure 2. Transfer Characteristics

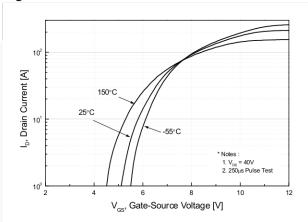
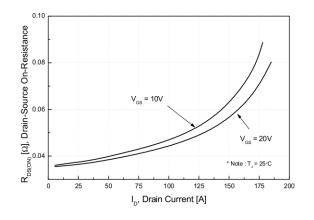


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue



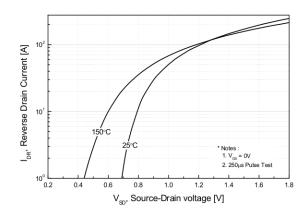


Figure 5. Capacitance Characteristics

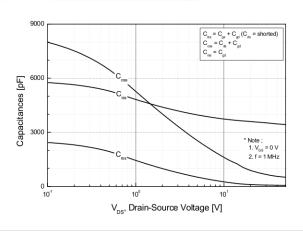
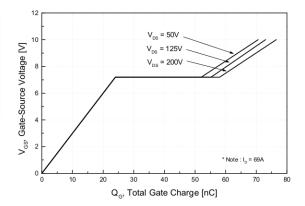


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

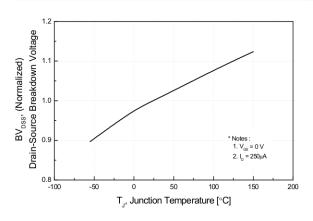


Figure 8. On-Resistance Variation vs. Temperature

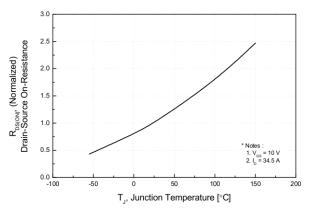
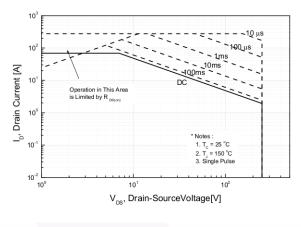


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature



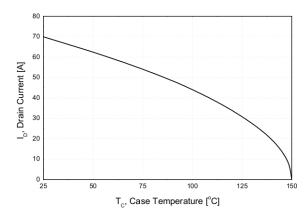
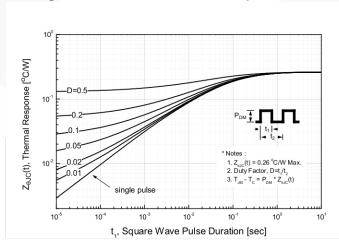


Figure 11. Transient Thermal Response Curve



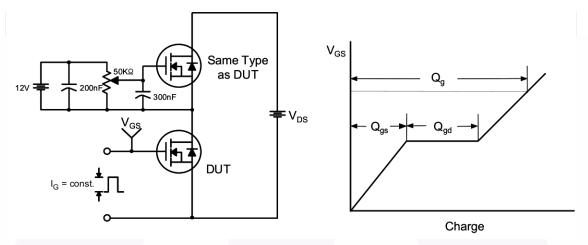


Figure 12. Gate Charge Test Circuit & Waveform

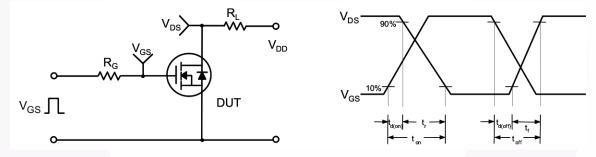


Figure 13. Resistive Switching Test Circuit & Waveforms

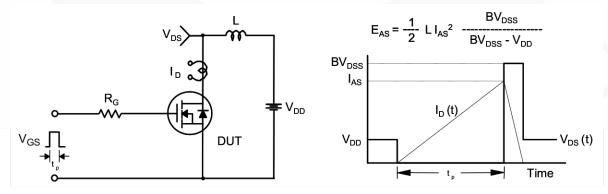


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

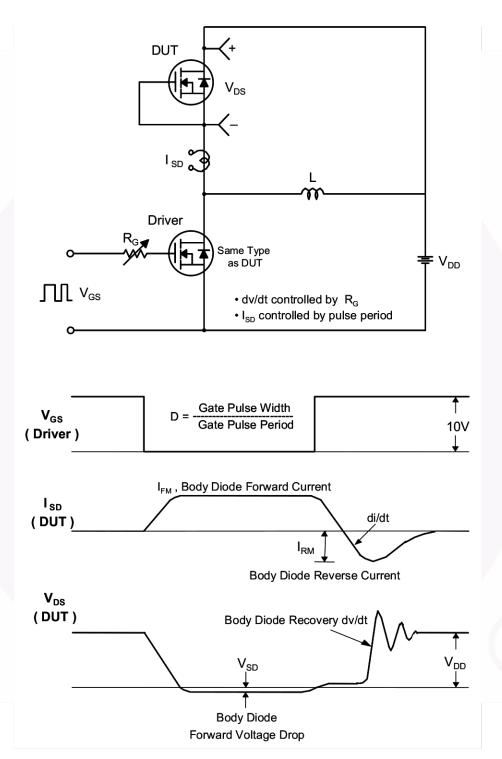


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

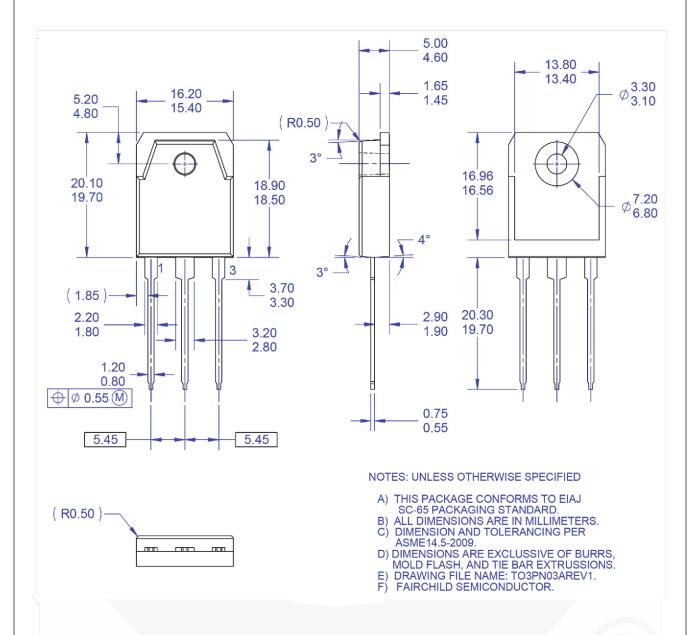


Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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