

Standard 16 A Triac

Datasheet - production data

Features

- Four quadrants
- Trigger current of 25 mA
- Package is RoHS (2002/95/EC) compliant
- Tab insulated, voltage = 2500 V rms
- UL certified (ref. file E81734)

Applications

- General purpose AC line load switching
- Home appliances:
 - Fan
 - Pump
 - Solenoid
- Lighting
- Heaters
- Inrush current limiting circuits
- Overvoltage crowbar protection circuits

TO-220AB insulated (T1625T-8I)

Table 1. Device summary

Order code	Quadrants	Value I _{GT} (mA)
T1625T-8I	1 - 11 - 111	25
110231-01	IV	50

Description

Available in TO220AB-Ins. (ceramic insulated), the T1625T-8I Triac can be used as on/off or phase angle function controllers in general purpose AC switching.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

Characteristics T1625T-8I

1 Characteristics

Table 2. Absolute maximum rating ($T_j = 25$ °C, unless otherwise specified)

Symbol	Parameter			Value	Unit	
	I _{T(RMS)} On-state rms current (full sine wave)		T _c = 108 °C	16		
T(RMS)			T _c = 119 °C	12	Α	
	Non repetitive surge peak on-state current (full	F = 50 Hz	t _p = 20 ms	120	Λ	
I _{TSM}	cycle, T _j initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	126	A	
l ² t	I ² t Value for fusing	•	t _p = 10 ms	95	A ² s	
V _{DRM} ,	Penetitive neak off state valtage gate energy		T _j = 150 °C	600	V	
V _{RRM}			T _j = 125 °C	800	V	
V _{DSM} , V _{RSM}	Non repetitive surge peak off-state voltage $t_p = 10 \text{ ms}$		T _j = 25 °C	900	V	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 100 \text{ Hz}$			100	A/µs	
I _{GM}	Peak gate current $t_p = 20 \mu s$ $T_j = 150 ^{\circ} C$		T _j = 150 °C	4	Α	
P _{G(AV)}	Average gate power dissipation $T_j = 150 ^{\circ}\text{C}$			1	W	
T _{stg}	Storage junction temperature range			-40 to +150	°C	
Tj	Operating junction temperature range			-40 to +150		
TL	Lead temperature for soldering during 10 s (at 4 mm from case for TO220AB-ins.)			260	°C	
V _{ins} (rms)	Insulation rms voltage, 1 minute, TO220AB ceramic insulated			2500	V	

T1625T-8I Characteristics

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Test conditions		Quadrant		Value	Unit
			I - II - III MAX.		25	
I _{GT} ⁽¹⁾	V 12 V B 20 O			MAX.	50	A
'GT \'	$V_D = 12 \text{ V}, R_L = 30 \Omega$	1 - 11 - 111	MIN.	1.25	mA	
			IV	MIN.	2.50	
V _{GT}	V_D = 12 V, RL = 30 Ω		All	MAX.	1.3	V
V _{GD}	$V_D = 800 \text{ V}, R_L = 3.3 \text{ k}\Omega, T_j = 125 \text{ °C}$		All	MIN.	0.2	V
I _H ⁽¹⁾	I _T = 500 mA			MAX.	35	mA
	1 101		I - III-IV	MAX.	40	mA
l _L	$I_{G} = 1.2 I_{GT}$				50	
dV/dt (1)	$V_D = 67\% \times 800 \text{ V gate open}$ $T_j = 125 ^{\circ}\text{C}$ $V_D = 67\% \times 600 ^{\circ}\text{V gate open}$ $T_j = 150 ^{\circ}\text{C}$			MIN.	500	V/µs
uv/ut · /					300	
(dl/dt)c (1)	V/dt)c = @ 10 V/μs	T _j = 125 °C		MIN.	4	A/ms
(di/di)C · /	(αν/αι)c = @ 10 ν/μs	T _j = 150 °C		IVIIIN.	2	
(dl/dt)c (1)	(dV/dt)c = @ 0.1 V/μs	T _j = 125 °C		MINI	12	Δ /
	$T_{j} = 150 \text{ °C}$			MIN.	6	A/ms
t _{GT}	gate controlled turn on time I_{TM} = 13 A, V_D = 400 V, I_G = 100 mA, dI_G/dt = 100 mA/ μ s, R_L = 30 Ω		All	TYP.	2	μs

^{1.} For both polarities of A2 referenced to A1

Table 4. Static characteristics

Symbol	Test conditions			Value	Unit
V _{TM} ⁽¹⁾	$I_{TM} = 22.6 \text{ A}, t_p = 380 \ \mu \text{s}$	T _j = 25 °C	MAX.	1.55	V
V _{to} ⁽¹⁾	Threshold voltage	T _j = 150 °C	MAX.	0.85	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	MAX.	30	mΩ
	T _j	T _j = 25 °C		5	μΑ
IDRM	$V_{DRM} = V_{RRM} = 800 \text{ V}$	T _j = 125 °C	MAX.	1	mΛ
IRRM	V _{DRM} = V _{RRM} = 600 V	T _j = 150 °C		3.6	mA

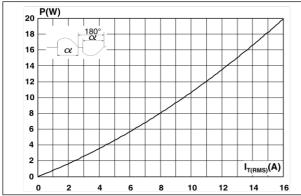
^{1.} for both polarities of A2 referenced to A1

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.1	°C/W
R _{th(j-a)}	Junction to ambient	60	°C/W

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Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case on-state rms current (full cycle) temperature (full cycle)



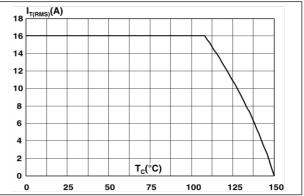
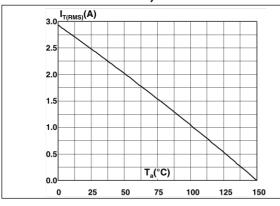


Figure 3. On-state rms current versus ambient temperature (free air convection)

Figure 4. Relative variation of thermal impedance versus pulse duration



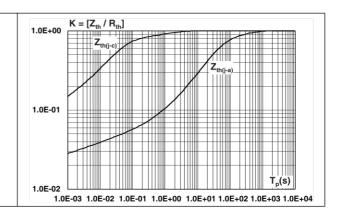
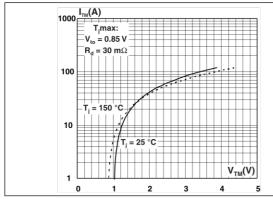
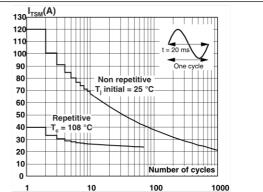


Figure 5. On-state characteristics (maximum values)

Figure 6. Surge peak on-state current versus number of cycles

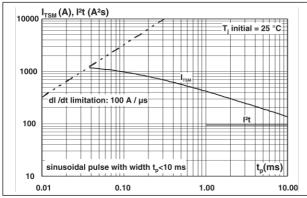




T1625T-8I **Characteristics**

Figure 7. Non repetitive surge peak on-state Figure 8. current and corresponding values of I²t

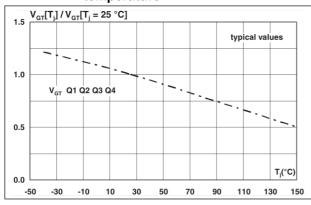
Relative variation of gate trigger current versus junction temperature



 $I_{GT}[T_j]/I_{GT}[T_j = 25 \text{ }^{\circ}\text{C}]$ typical values 3.5 I_{GT} Q4 3.0 I_{GT} Q3 I_{GT} Q1-Q2 2.0 1.5 1.0 0.5 T_i(°C) 0.0 -50 -30 -10 10 30 110 130

Figure 9. Relative variation of gate trigger voltage versus junction temperature

Figure 10. Relative variation of holding current and latching current versus junction temperature



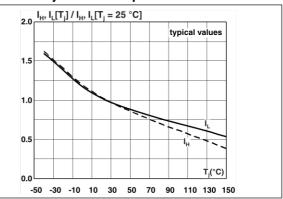
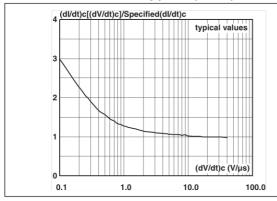
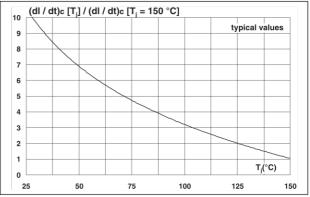


Figure 11. decrease of main current (di/dt)c versus reapplied (dV/dt)c

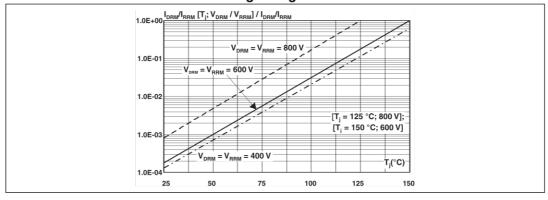
Relative variation of critical rate of Figure 12. Relative variation of critical rate of decrease of main current (di/dt)c versus junction temperature





Characteristics T1625T-8I

Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage



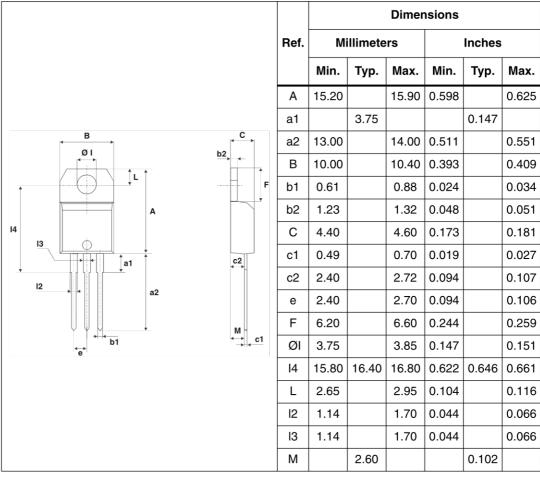
T1625T-8I Package information

2 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N·m

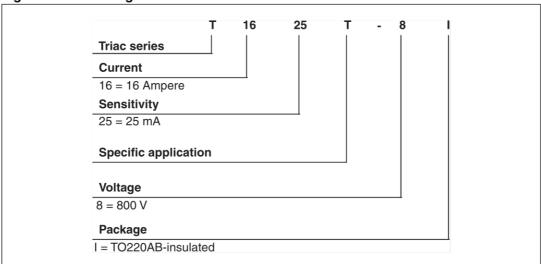
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Table 6. TO-220AB (Nins. and ins. 20-up) dimensions



3 Ordering information scheme

Figure 14. Ordering information scheme



4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1625T-8I	T1625T-8I	TO-220AB insulated	2.3	50	Tube

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
20-Jan-2012	1	First issue.
25-Apr-2012	2	Updated UL certification.

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